

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Hideomi SUZAWA et al.

Serial No. 10/086,628

Filed: March 4, 2002

For: SEMICONDUCTOR DEVICE

AND MANUFACTURING METHOD

THEREOF



Art Unit: 2814

Examiner: N. Ngo

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I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 17, 2003.

Name: Adele M. Stamper

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of May 19, 2003, please amend the subject application as follows:

IN THE CLAIMS:

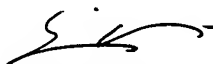
Cancel claims 1-7 without prejudice to applicant's right to file a divisional application with respect thereto.

REMARKS

In response to the election requirement in the Office Action of May 19, 2003, Applicant's hereby elect without traverse the Group II claims - that is, claims 8-15, drawn to a process for making a semiconductor device.

Examination on the merits is requested.

Respectfully submitted,


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